Evaluation Net Simulation System
Reference Manual*

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ABSTRACT

This report is a reference manual describing the capabilities, limitations, and usage of the University of Colorado Evaluation Net Simulation System. This software package implements a simulation programming language which is composed of a control flow graph model integrated with the text of the program. The implementation is composed of four subsystems: a graphic net editor, a compiler, a link editor, and an interpreter.

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I. INTRODUCTION

The class of evaluation nets has been previously formulated to aid in an orderly approach to simulation model building. In this document, it is assumed that the reader has already familiarized himself with the nets and is interested in the corresponding implementation of an evaluation net interpreter.

Briefly, the graph structure of the net and the various procedures define a machine interpretable model. The use of a graph structure as a partial definition is a visual aid to the model designer, and should be an integral part of the implementation. The model designer can then carry on an interactive session with a graphics package to derive the control structure of the model. Subsequent sessions are used to refine the graph model and to define the various procedures for the net. Once an approximation to the design is complete, the user can submit the entire net to an interpreter. The interpreter should then execute the model, given an appropriate set of data. Monitoring the interpretive execution could be done on-line with a suitable graphics console, or it could be an interpreter function with no user intervention to the interpreter once it begins to exercise the net.

The implementation described in this report is for the University of Colorado CDC 6400 system running under the KRONOS operating system. The only special peripheral device required is an interactive graphics console, and in this case, the device we used is a Tektronix 4010 remote display station. This unit is a cathode ray tube device with images stored on the surface of the screen, and thus it is reasonable to employ the device as a remote graphics console with low speed (300 baud) communication lines. A refresh scope could provide a wider

variety of displays if it included suitable local storage and computing power (e.g. a DEC GT-40 with disk auxiliary storage).

The software is primarily composed of FORTRAN IV routines, with the remaining portions of code being written in the CDC 6000 assembly language, COMPASS. An attempt has been made to keep the FORTRAN code reasonably machine independent, those areas relying on word size, etc. being commented as such in the listing. The ANSI standard was also used in writing the FORTRAN programs. One goal of this implementation was to make the programs portable, with a minimum of effort, to either another medium-to-large scale system or a minicomputer system. (It was initially admitted that a minicomputer implementation might be very slow.) The portability constraint was the primary reason for choosing FORTRAN over various other languages available at this installation.

The CDC 6400 - KRONOS implementation of the Evaluation Net Simulation System (ENSS) relies heavily on the permanent file system of KRONOS. All intermediate files between ENSS subsystem steps are saved on KRONOS files. The KRONOS text editor may be used on some of the E-net files.

ENSS is composed of four distinct subsystems (See Figure I.1): ENETDIS, the graphics subsystem for interactive net construction and editing; COMPLR, the text compiler which produces interpretive code to simulate the net; LINK, a link editor which combines two or more nets that have been previously compiled; and INTERP, an interpreter to execute the net. In the following four sections each of the individual subsystems will be discussed with respect to their capabilities, limitations, and usage. Detailed subsystem documentation is not included in this manual.

The following graduate students have participated in various parts of the ENSS-Version 1 implementation: M. Bailey, W. Ellis, and J. Read.

3. CDC, "KRONOS 2.0 Reference Manual", publication No. 59150600.
II. THE DISPLAY AND EDIT SUBSYSTEM

A. Capabilities and Limitations

The display and edit subsystem, ENETDIS, is an interactive subsystem designed for use with the Tektronix 4010 display station, (or similar storage scope graphics devices). The purpose of ENETDIS is to allow a user to interactively create and edit the graph structure of an E-net, using the display screen for graphic output and the keyboard for net schema descriptions. The basic design of this system assumes that the user will provide the identification of a set of associated locations with each introduction of a transition; furthermore, the user ordinarily provides the cartesian coordinates of the geometric center of the transition when addressing a transition-location set. ENETDIS does have the capability of constructing a graph structure in the absence of the aforementioned cartesian coordinates, although the transitions are placed on the screen by algorithmically determined cartesian coordinates (rather than user-provided placement).

The subsystem essentially operates in two modes, approximately corresponding to interactive and batch mode. In the interactive mode, ENETDIS input is a set of commands and data which direct the generation of net components, or cause the subsystem to alter currently defined components of the E-net being operated upon. To generate components for the net, the interactive user provides a schema type, transition and location labels, and a cartesian coordinate for the center of the transition. The transition-location set is entered into the description of the current net. Interactive editing of the current net allows the user to change the coordinates (screen position) of any transition-location set as well as the ability to change or rename the transitions or locations.
The "batch mode" of ENETDIS is executed from the graphics console, but causes the subsystem to display an entire E-net from a permanent file containing a text description suitable as input to the compiler subsystem. This format of input does not include screen position coordinates, hence an algorithm is used to compute transition-location set coordinates; the result is that the display often needs to be interactively edited in order to produce a more attractive display.

ENETDIS interprets commands that allow the user to fetch and save KRONOS permanent files in one of two formats called binary format or text format. The text format is the usual (CDC 6000 display code) character string representation described above and is appropriate when the user either wishes to display compiler-input text in the batch mode, or to save an E-net in a format in which the compiler can use. The overhead involved with this file format is caused by the necessity for ENETDIS to convert its internal graph representation either to or from the external textual representation. In a series of console sessions to design the graph structure of a model, it is necessary that intermediate net descriptions be saved; rather than employing the representation transformations to convert between internal and external representations, the user can save an E-net in binary format, (which can be used as subsequent input to ENETDIS).

The level of sophistication incorporated in a graphics display can grow rapidly with a few added constraints. ENETDIS is not a highly sophisticated software tool, but is intended to be flexible enough to aid in E-net graph structure design. The primary limitations imposed by this simplistic approach are machine and installation dependencies in the form of display driver routines.
The line positioning algorithm could stand some improvement, as it allows lines to pass through transitions and locations. Occasionally, lines also coincide. Another significant liability of ENETDIS is its inability to display large E-nets; a windowing process needs to be incorporated to allow for arbitrarily large (in space) displays.
B. Using the Display Subsystem

In the following discussion, at least a superficial understanding of the CDC 6400 - KRONOS system is assumed. Prerequisite knowledge can easily be obtained from the KRONOS Time-Sharing Reference Manual.

ENETDIS is executed under the batch subsystem of KRONOS, but must be called under the TELEX subsystem with the INPUT file corresponding to the display terminal used to initiate the conversation. The following memory requirements (specified as octal numbers) are required:

33100 words to compile
37600 words to load
23200 words to execute.

This execute field length provides for about 100 locations and about 25 transitions, which is likely to be sufficient with the display space limitation discussed earlier.

After logging into the TELEX time sharing system, the following dialogue should ensue, (system output is underlined, and \texttt{\(\text{R}\)} is a carriage return):

\texttt{SYSTEM: GET,ENET/UN=X652\(\text{R}\)}

\texttt{READY}

\texttt{UNFMT\(\text{R}\)}

\texttt{READY}

\texttt{CALL,ENET\(\text{R}\)}

The last control command shown is a call to a KRONOS procedure file composed of the following set of control cards (executed in the batch subsystem):
GET,TNET/UN=X652.
ATTACH,T4CUG,T4010/UN=LIBRARY.
RFL(40000)
LOAD,TNET,T4CUG,T4010.
ENETDIS.

TNET is a permanent file containing the object code of ENETDIS, and T4CUG and T4010 are library files containing (machine and installation dependent) Tektronix 4010 display routines.

Upon executing the "ENETDIS." command in the TNET procedure file, control is passed to the display and edit subsystem, ENETDIS. The subsystem responds by erasing the screen and writing the request

COMMAND

The user must now respond with any of one of the following commands, followed by a carriage return, (illustrated in Tables II.1 - II.5):

Transition-Location Creation Commands

Each of these commands cause a new transition-location set to be introduced into the display data base. Each command causes ENETDIS to request an x-y pair (0 ≤ x ≤ 1000, 0 ≤ y ≤ 700) to determine the screen position for the geometric center of the transition by printing the message

POS.

The user must respond with an x-coordinate value, blank, y-coordinate value, and carriage return. ENETDIS then requests a list of labels for the transition-location set by printing the message

LABELS.
The labels must then be entered in the following order: transition label, input locations from top-to-bottom and output locations from top-to-bottom. Labels should consist of 1 to 3 alphanumeric character. Elements of the list are delimited by blanks, and the list is terminated by a carriage return. A run-time FORTRAN diagnostic results if an inappropriate number of labels are entered by the user.

\[ \alpha \text{(R)} \]

Construct the display structure for an \( \alpha \)-transition and associated locations; \( \alpha \) is F, J, T, X, or Y.

**Editing Commands**

This set of commands provides the capability for changing transition labels, positions, and connections and for changing the name of a location. (Location positions on the screen are algorithmically determined by the screen positions of associated transitions.) The current screen image is not altered by the following commands, although the display data base does reflect the editing.

`CHANGE (R)` Allows the user to change any of the attributes (other than schema type) of a transition. System response to the command is

`TRANS NAME`

The user must then enter zero (0) to escape the CHANGE command or a transition name. If the name is not found in the data bank, "NOT FOUND, RETRY" will be printed and the system will wait for another transition name. When a declared transition name is entered, ENETDIS next requests new position and label values by the message

`POS, LABELS`
The user must respond with a list of x-y coordinates and appropriate labels (see creation commands), delimited with blanks and terminated with a carriage return. If an attribute should not be altered, enter "NC" in the appropriate position in the list. The display structure is modified, although the screen image of the net is not altered unless the DISPLAY command is invoked.

DELETE  

Allows the user to remove a transition from the active display data bank. The response to this command is

TRANS NAME

The user must then respond with zero (0) to escape the DELETE command or a transition name. If the name is not found in the data bank,

NOT FOUND,RETRY

will be printed and the system will wait for another transition name. When a declared transition name is entered, the corresponding transition is removed from the active display data bank.

LOCHANGE  

This command causes a location name to be changed. System response to the command is

OLD NAME

The user must then enter zero (0) to escape the LOCHANGE command or a location name. If the name is not found in the declared location list,

NOT FOUND,RETRY
is printed and the system requests another location name. Otherwise, system response is

```
NEW NAME
```

to which the user should enter the new name and a carriage return.

A transition can be replaced with a transition of another type at the same screen position using this command. System response is

```
TRANS NAME
```

The user may escape the REPLACE command by entering zero (0), or he may enter a transition name. If the name is not found in the declared transition list,

```
NOT FOUND,RETRY
```

will be printed and the system will wait for another transition name. When a previously declared transition name is entered, the corresponding transition is DELETED from the transition list. ENETDIS then requests

```
NEW TYPE
```

expecting one of "F", "J", "T", "X", or "Y" to be entered by the user. The old transition screen positions is used but the system requests new labels by

```
LABELS
```

The user should then enter a new label list (i.e. transition name, input location names, output location names) delimited by blanks and terminated by a carriage return.
Display Directives

This class of ENETDIS commands controls the amount of information displayed on the screen with respect to the current E-net defined in the display data base. The system incorporates a display switch which allows the user to enable/disable the drawing process. The default value of the switch is "off", i.e. transition-location set creation does not cause an image to appear on the screen unless the DISPLAY command is involved.

- **DISPLAY**
  - The current screen image (including commands) is erased, and redrawn from the current display data base. The value of the display switch is not affected.

- **DRAWOFF**
  - Sets the value of the display switch to "off".

- **DRAWON**
  - Sets the value of the display switch to "on". Editing commands remain display-disabled regardless of the value of the display switch.

- **FLUSH**
  - Clears the display data base, thus enabling a new E-net to be created or edited.

- **GRID**
  - Displays the cartesian coordinates for the screen. The coordinate labels are divided by 100.

Permanent File Commands

The permanent file commands are included to enable the user to get and save E-nets on files in either binary or text formats. The system responds to these requests by

```
PF NAME,FORMAT
```

to which the user should respond with a legitimate KRONOS permanent file name.
and "B" for binary format or "T" for text format. An incorrect format response will result in a program abortion. A zero (0) file name is used to escape the file command. TAPE1 is used as the local file for the GET and SAVE operations. Since the files are saved as private files, all GETS on a file must be done under the same user number as the SAVE.

GET (R)  Set the display data base to contain the E-net on the given permanent file. This command destroys the current display data base.

SAVE (R)  Save the current display data base on a permanent file. (The command actually replaces the named file).

PURGE (R)  Purge the named permanent file from the system. This command is equivalent to the KRONOS purge command.

Miscellaneous Commands

END (R)  Terminates ENETDIS and returns control to the KRONOS operating systems. The display program can be immediately reloaded and executed by entering "ENETDIS."

HELP (R)  Erases the screen and provides a synopsis of all commands and execution directions.

The following set of plates illustrates the use of some of the above commands.
Result of the HELP Command

Table II.1

COMMAND ? HELP

COMMAND-

CHANGE - CHANGE ALL BUT TYPE OF TRANSISTION (NC FOR NO CHANGE
DELETE - DELETE TRANSISTION FROM NET
DISPLAY - ERASE THE SCREEN AND REDRAW THE CURRENT NET
DRAWOFF - TURN OFF THE DRAW SWITCH **DEFAULT VALUE**
DRAWON - TURN ON THE DRAW SWITCH
END - TERMINATE ENETDIS
FLUSH - CLEAR PRESENT NET AND RESTART INPUT
GET - RETRIEVE A PREVIOUSLY SAVED NET
GRID - MARK THE GRID COORDINATES ON THE SCREEN
HELP - PRINT THIS LIST
LOCHANGE - CHANGE THE NAME OF A NODE IN NET
PURGE - PURGE A PREVIOUSLY SAVED NET
REPLACE - REPLACE A TRANSISTION WITH A NEW ONE AT SAME PLACE
SAVE - SAVE A NET AS A PERMANENT FILE

TO RUN ENETDIS-

SYSTEM-GET,ENET/UN=x652
UNFMT
CALL,ENET

TO RERUN THE PROGRAM, ENTER ENETDIS AND CR
Entering a Transition - Location Set

Table II.2

COMMAND ? DRAWON
COMMAND ? X
POS ? 300 300
LABELS? A1 R1 B1 B2 B3
COMMAND ?
The Addition of Transition A2

Table II.3

(Note that the change command does not affect the display)
The Result of the CHANGE Command

Table II.4

COMMAND ? GRID
COMMAND ? SAVE
PF NAME,FORMAT? NET1 T
COMMAND ?

6  1  2  3  4  5  6  7  8  9  10

5

4

3

2

1
OLD, NET1
READY.
LNI

TYPE TRANS

A1: X(R1,B1,B2,B3);
A2: J(B2,B3,B1);

END.
READY.
III. THE COMPILER SUBSYSTEM

A. Capabilities and Limitations

The compiler subsystem, COMPLR, translates an E-net formal description into an abstract machine language which is ultimately executed by the interpreter abstract machine. The abstract machine program includes data structures to represent the set of locations, the set of transitions, the set of environment variables, and a body of two-address abstract machine instructions representing transition procedures, time expressions, and resolution procedures. When the compiler completes the translation process, the abstract machine program is written to a KRONOS file, where the ENSS user may dispose of the file (TAPE7) as he sees fit, (e.g. the file may be immediately executed by the interpreter, or it may be saved as a permanent file for later use). The compiler output is a complete data structure for interpretation purposes, but may also be treated as subnet and be linked to another subnet to form a composite abstract machine language program. The details of the linking process are discussed in Section IV.

The compiler translates an input string <evaluation net> as defined by the BNF syntax given in Appendix B. The compiler design has been influenced by classic table-driven compiler design. The major modules are a lexical analyzer, syntax analyzer, code generator, and output routine.

The line printer output from COMPLR includes a listing of the input stream along with error indicators for constructs that violate the syntax of the source language (see Table III.1). The existence of syntax errors can confuse the syntax analyzer to the point that subsequently compiled expressions will be interpreted as errors, even though they may be correct. This is not a serious liability, since the faulty compilation takes place only after a true syntax error has been encountered.
The current version of the compiler will handle a maximum of 160 locations, constants, and environment variables, or about 130 transition, (each transition description requires a variable number of entries, depending on the transition type; the transition description array elements are dynamically allocated by transition type). The maximum number of abstract machine instructions that can be generated per compilation is 256, (see Appendix C for a description of the abstract machine and language).

The "macro net capability" discussed elsewhere is currently not implemented in ENSS. Subnets must be used to invoke repeated net patterns. It is also required that environment variables be limited to non-array values, i.e. an environment variable may not be declared as an array. The formal syntax accepted by the compiler is given in Appendix B. A summary of the differences between the ENSS implementation and the E-net formulation is given in Appendix A.

B. Using the E-net Compiler

The compiler subsystem resides on a KRONOS permanent file named COMPLR, and must be executed in the batch subsystem, either from a terminal or via the usual batch stream. The following octal memory requirements apply:

34700 words to compile
34400 words to load
24600 words to execute

This run-time field length allows for approximately 130 transitions, using expressions and procedures that compile to less than or equal 256

abstract machine instructions, (see Appendix C). The text file containing
the net description must be on TAPE5 (which is not equivalent to the INPUT
file). The load module is written to TAPE7 and a listing of the net is
printed on the OUTPUT file device. Thus, the following control cards would
compile the net found on KRONOS permanent file TXTNET (in text format), and
the load module would be saved on the permanent file named LDMOD:

```
.:.
GET,COMPLR/UN=X652.
GET,TAPE5=TXTNET.
COMPLR.
SAVE,TAPE7=LMOD.
```

The order in which various components of the net are described is
rigid; any order of TYPE declarations other than that given here will
result in compiler errors. A simple example (described later) is given
in Table III.2. All input data are free field, with delimiters.

1. TYPE LOC: <d.list>;

Mandatory location declaration list. The <d.list> is composed
of individual location declarations for each location that appears
in the net. Each declaration is delimited by commas, and the
declaration list is terminated by a semicolon. A token with one
or more attributes (i.e., an attribute token)
is declared by stating the name followed by the number of attributes
enclosed by parentheses. Simple tokens (with no attributes) are
declared with no parenthesis notation.

2. TYPE PERIPH: INPUT=<list> OUTPUT=<list> RESOL=<list>;

Mandatory declaration of input, output, and resolution peripheral
locations. Input and output locations must have been previously
declared in the TYPE LOC: section. A <list> is composed of location
labels separated by commas and terminated with a blank. Any field
in the declaration may be omitted by deleting the key phrase and list. Each location declared to be an INPUT peripheral location will have a "generator net" concatenated to the input edge of the location. This generator produces a constant saturation load of simple tokens to the corresponding INPUT location. Each OUTPUT peripheral location has an "absorber net" appended to its output location which removes a token from the corresponding location immediately after the token takes up residence. The absorber net contains attribute locations that maintain a running sum of corresponding attribute values on the output location and an additional attribute to count the number of tokens that constitute the attribute sums. Additional transitions and locations introduced to the net by the absorber/generator nets appear in interpreter output under the labels of ZZZZi, for 10<i<99.

3. TYPE ENVIR: <list>;

An optional declaration of a <list> of simple environment variables (no arrays of environment variables are permitted). These variables may be referenced in any procedure.

In the following declarations, resolution procedures, time expressions, and transition procedures will use six "built-in" functions: EMPTY (loc), FULL (loc), RANDOM (seed), LOG (x), EXP (x), and EXTF (i,x). EMPTY and FULL evaluate to true or false, depending upon whether the argument location is empty or full, (e.g. EMPTY (B) = true if location B is empty, otherwise it evaluates to the value false). RANDOM (seed) samples a pseudo random number sequence with the given seed. LOG (x) evaluates to the natural logarithm of the argument X and EXP(x) evaluates to e^x. EXTF (i, x) is a function call to a user-provided FORTRAN function
to be loaded with the interpreter. Normally, i would be used to
denote any one of a number of external functions to be evaluated, and x
is the actual argument.

4. TYPE \_\_ RES\_L: R1: <rproc>; R2: <rproc>; ... Rn <rproc>;
Resolution procedures must be defined for each peripheral resolution
location Ri declared in the TYPE \_\_ PERIPH: declaration. Each <rproc> is a
resolution procedure (formally defined in Appendix B) which provides
conditions under which the resolution location can become set, reset, or
undefined.

5. TYPE \_\_ TRANS: A1: <schema>, <time expr>, <tr proc>;
   A2: <schema>, <time expr>, <tr proc>;
   ... Am: <schema>, <time expr>, <tr proc>;
   END

Mandatory transition declaration section. The formal syntax for this
implementation differs somewhat from the original syntax; differences are
noted in Appendix A, and the syntax is given in Appendix B. The <time
expr> or <tr proc> may be null, implying zero firing time and default
transition procedures respectively. Otherwise, <time expr> is an arithemtic
expression, evaluated before the transition fines, which defines the firing
time. The <tr proc> defines a procedure to be executed at transition
firing termination. The procedure should be written with the understanding
that the tokens have already been moved. The <schema> defines the
transition type and locations.

A summary of errors that the compiler can detect is given in Table III.1.
Suppose that it is desirable to create an E-net model of the following (trivial) system: there exists a finite queue with only three queue locations. The queue has a poisson arrival pattern with an arbitrary mean interarrival time, i.e., the mean value is a simulation parameter. The queue is served by two identical processors, with the service time being uniformly distributed, again with an arbitrary mean value. Figure III.1 is a graph of the E-net (produced by ENETDIS), where locations B3, B4, and B5 represent the (first-come-first-served) queue. Location B6 represents the condition that server number 1 is busy, and a token on B7 represents the condition that server number two is busy. The text of the net might be given as shown in Table III.2.
Table III.1
Error Diagnostics

1. Left part of an expression must be a location or environment variable.
2. Store operator missing.
3. Left paren missing.
4. Location subscripts must be constants.
5. Right paren missing.
6. Colon missing.
7. Comma missing.
8. Relational operator missing.
9. Location name not a legal identifier.
10. Undeclared location or environment variable.
11. Expression evaluation stack overflow.
12. Incomplete conditional statement (THEN missing).
13. Semicolon or IF missing.
14. Semicolon or ELSE missing.
15. Semicolon missing.
16. Transition type violation.
17. Undeclared resolution location.
18. Unused.
19. Ill-formed Resolution location or environment variable.
20. Unused.
21. Resolution procedure missing.
22. Attribute reference to simple location.
23. Ill-formed vector-copy command.
A Double Server Queue

Table III.2

TYPE LOG: B1(2), B2(2), B3(2), B4(2), B5(2), B6(2), B7(2), B8(2), B9(2);
TYPE PERIPH: OUTPUT=B3, B9 RESOL=R1;
TYPE ENVIRO: MNAR;
TYPE RESOL:
   R1: IF EMPTY(B7) THEN R1:=1 IF EMPTY(B6) THEN R1:=0;
TYPE TRANS:
   A1: T(B2, B1), B1(1):=MNAR+LOG(RANDOM(0));
   A2: F(B1, B3, B2), B1(1), B3(2):=B2(2)*RANDOM(0);
   A3: T(B3, B4);
   A4: T(B4, B5);
   A5: X(R1, B5, B6, B7);
   A6: T(B6, B6), B6(2);
   A7: T(B7, B9), B7(2);
END
A Double Server Queue

Figure III.1
IV. THE LINK EDITOR

A. Capabilities and Limitations

The compiler subsystem produces a file (called the load module) containing all data structures in a format suitable for input to the interpreter. This set of data structures is relatively complex, and contains many cross-references between different parts of the data structure. (These cross-references can be thought of as being absolute addresses.) The link editor has been provided to combine a set of up to five distinct load modules, producing a single load module in the format suitable for input to the interpreter. Thus, the link editor must combine the various data structures, resolving new absolute addresses, and deleting absorber/generator nets from peripheral locations as defined by the net composition.

The load modules to be linked together are passed to the link editor on TAPE1, TAPE2, ..., TAPE5, and the combined-net load module is on TAPE98 when the link editor terminates. Directives for the linking process are entered via the usual INPUT file. The OUTPUT file will contain a listing of the directives, and a load map if requested by the MAP directive discussed in the usage section. Regardless of the output option chosen, edit warnings indicating location dimension specification conflicts will be written to the OUTPUT file.

The link editor is implemented to handle a resulting net with a maximum size as determined by the current version of the interpreter program. The size limitation arises due to the lack of a "paged memory" for the net description.

B. Usage

The link editor subsystem is saved on a permanent file named LINK. This file may be executed from a terminal or the card reader in the KRONOS batch subsystem. The following octal memory requirements apply:

34500 words to compile
51400 words to load
43100 words to execute
The link editor maintains a central memory image of the resulting combined load module; the load module is thus constrained to 320 locations, constants, and environment variables. The abstract machine code for all procedures must not exceed 1536 instructions (see Appendix C). The load module for the first subnet must appear on TAPE1, the load module for the second net must appear on TAPE2, etc. No more than five subnets may be combined on any one run of LINK. TAPE98 contains the combined load module when LINK terminates. Suppose that one wished to combine two subnet load modules on files named NETB11 and NETB13; the resulting net is to be saved on a file named BIGNET. The following sequence of KRONOS control cards will accomplish this task:

```
.
.
GET(TAPE1=NETB13,TAPE2=NETB11)
GET(LINK/UN=X652)
LINK.
SAVE(TAPE98=BIGNET)
.
.
```

The following link editor directives are passed to the program on the TAPE99=INPUT file. The order of directives given below is rigid, and must not be violated:

1. LINK (output net name)
   Mandatory directive which specifies the name of the net in load module format on TAPE98 when the program terminates.

2. MAP (=)
   Optional load map content indicator. If the MAP directive is omitted, no load map will be produced.
   
   =P Partial load map. A summary of the nets to be combined is given,
i.e., names, number of involved peripheral locations, the appended character for unique transition/location names, and table sizes. A similar summary is provided for the resulting net composition.

==F Full load map. The net summaries discussed in the partial load map option are included with additional detail, e.g., additional subfield lengths in the location table are provided. A table of subnet interconnections summarizes location name, status, dimension, and orientation. This option also dumps the transition table and location table to the OUTPUT file. Details of these tables are discussed in the Appendix C.

3. SUBNETS (net_1=B_1, net_2=B_2,...,net_n=B_n)
Mandatory subnet declaration list, where 1<n<5. The net whose name is given by net_1 is expected to be passed to the program on TAPE_1. B_1 is an appendage character that prefixes each location name in the load module on TAPE1. For example, if B3 is produced on TAPE4 by the compiler, and NETNM=X is the fourth net name in the declaration list, any other directive must refer to the location as XB3.

4. BLoc_i TØ BLoc_j
Mandatory directive to the link editor requesting that the output peripheral location BLoc_i is to become the same location as the input peripheral location BLoc_j.

5. END
Mandatory directive to terminate the input string.

Table IV.1 is a composition of the example net in section III and a simple absorber net. The output option is full.
LINKAGE EDITOR
FULL MAP:

LINK (COMBIN)
MAP (F)
SUBNETS (NETB1=A, ABSBI=R)
ABB TO BB1
END

EXTENT OF LINKED OUTPUT E-NET:

TRANS TABLE SIZE 140

LOCATION TABLE:
(LLOCATION) SIZE 18
(EV/CONST) SIZE 13
(OVERALL) SIZE 31

CODE TRIPLES NUMBER 159

USAGE LISTS SIZE 0

INVOLVED PERIPHERALS 2

EDIT WARNING:
DIMENSION CONFLICT BETWEEN
OUTPUT PERIPHERAL ABB (2)
INPUT PERIPHERAL BB1 (3)
DIMENSION 3 RETAINED FOR BB1

NEW SUB-NET FILE COMBIN **

REFERENCED SUB-NETS:
NETB1 ABSBI

LINKED PERIPHERALS PER SUB-NET:
1 1

APPENDED CHARACTER PER SUB-NET:
A B

EXTENT OF EACH SUB-NET:

<table>
<thead>
<tr>
<th>SUB-NET</th>
<th>TRANS TABLE</th>
<th>MID LOC TABLE</th>
<th>LOC TABLE</th>
<th>CODE TRIPLES</th>
<th>USAGE LISTS</th>
</tr>
</thead>
<tbody>
<tr>
<td>NETB1</td>
<td>109</td>
<td>14</td>
<td>19</td>
<td>97</td>
<td>1</td>
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<tr>
<td>ABSBI</td>
<td>73</td>
<td>10</td>
<td>16</td>
<td>64</td>
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</table>

LINKAGE STRUCTURE:

<table>
<thead>
<tr>
<th>SUBNETS PERIPH</th>
<th>STATUS TO SUB-NET</th>
<th>PERIPH</th>
<th>DIMENSION</th>
</tr>
</thead>
<tbody>
<tr>
<td>NETB1 ABB</td>
<td>OUTPUT ABSBI BB1</td>
<td>2</td>
<td></td>
</tr>
<tr>
<td>ABSBI BB1</td>
<td>INPUT NETB1 ABB</td>
<td>3</td>
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</tbody>
</table>
LINKED NET:

TRANSITION TABLE:

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<tr>
<th>INDEX</th>
<th>TYPE</th>
<th>NAME</th>
<th>TRPR</th>
<th>FTIME</th>
<th>UCC/EN</th>
<th>A-LOC</th>
<th>A-TR</th>
<th>C-LOC</th>
<th>C-TR</th>
<th>B-LOC</th>
<th>B-TR</th>
<th>RESOL</th>
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<td>1</td>
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<td>AA1</td>
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<td>3</td>
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<td>0.000000</td>
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<td>5</td>
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LOCATION TABLE:

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<tr>
<th>INDEX</th>
<th>STOR</th>
<th>PTR</th>
<th>NAME</th>
<th>DIM/USE</th>
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</thead>
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<td>0.0000002</td>
</tr>
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<td>0.0000002</td>
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<td>0.0000002</td>
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<td>0.0000003</td>
</tr>
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<td>89</td>
<td>AZZZ12</td>
<td>0.0000003</td>
</tr>
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<td>PACKED WORD</td>
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<td></td>
<td></td>
</tr>
<tr>
<td>-------</td>
<td>-------------</td>
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<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>0000000000000000</td>
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<td></td>
<td></td>
</tr>
</tbody>
</table>

**USAGE LIST VECTOR:**

16  100  AZZZ13  00000003
17  53   BZZZ12  00000004
18  64   RZZZ13  00000004
19  16   AR1     00000001
20  0    AMNAR   00000000
21  2    A2      77777776
22  1    A1      77777776
23  0    A0      77777776
24  13   AR1     00000001
25  0    RTEST1  00000000
26  0    RTEST2  00000000
27  2    B2      77777776
28  1    B1      77777776
29  5    B5      77777776
30  0    B0      77777776
31  10   B10     77777776
V. The Interpreter Subsystem

A. Capabilities and Limitations

The E-net interpreter program, INTERP, implements the abstract machine discussed in Appendix C. The first level of the abstract machine handles token flow through the network which has been previously compiled, (and possibly link edited with other subnets), by COMPLR. The tokens represent the data structures which may have their values referenced and altered through transition procedures, resolution procedures, and integer-valued time expressions. Resolution procedures and time expressions can only reference the data structures when determining the token flow through a net. The second level of the abstract machine implements data structure access and modification.

INTERP reads a net in the internal format from the file named TAPE1; the usual situation would be that TAPE1 is the same file as TAPE7 produced by the compiler. The user must provide interpreter specifications on the INPUT file. INTERP then simulates E-net activity using discrete simulation techniques to handle transition firing, and interpreting data structure manipulation procedures. Each transition specification dictates an expression of the amount of time that a particular firing should remain in the active phase. The token flow through the net determines the time at which the transition should become active, i.e., the enabling time is determined by the token flow. The active phase is completed by the termination procedure for the transition, in which tokens are moved and the transition procedure is evaluated. Thus, transition simulation is natural using an event approach; the first event of a firing corresponds to the enabling of the transition firing sequence in the net.
The user has the capability of obtaining a full trace of the transition firing sequence, or he may suppress the trace and only receive statistics at simulation termination. The net can be exercised repeatedly using different initial markings; environment variable initial values are also controlled by the input specifications. The initial simulation clock time is an input parameter, as well as the simulation termination time. All arithmetic (except for clock manipulation) is floating point.

The current version of the interpreter provides summary information indicating total and average dwell time for each location, including the system-provided absorber/generator locations (labeled ZZZ's). The location throughput count is also provided. The absorber locations contain tokens with attributes that automatically sum the values of the corresponding output peripheral location and the respective attribute average values. Finally, the list of transitions that are active at simulation termination is printed on the OUTPUT file.

Improvements to the output of INTERP might include a set of histograms specified by the USER. It might also be possible to include a turnaround time and throughput rate specification for user-provided paths through an E-net.

A user-provided FORTRAN function subprogram may be loaded with INTERP to provide for extended capability in each particular simulation. The details for using the subprogram in E-net procedures was given in Section III.B.

A useful programming improvement to the current version of INTERP would be to incorporate overlays into the code; the initialization of the program requires a significant amount of space.
B. Usage

The interpreter subsystem resides on the KRONOS permanent file named INTERP, and must be executed in the batch subsystem, either from a terminal or via the usual batch stream. The following octal memory requirements apply:

34500 words to compile
47600 words to load
30200 words to execute

Note that these figures use a small (dummy) version of EXTF. Version 1 of INTERP provides sufficient memory to handle a maximum of 320 locations, constants, and environment variables; depending on the number of each type of transition allowed; this is equivalent to approximately 250 transitions. The procedures must compile to less than or equal to 1536 instructions. The net in compiled format is read from TAPE1; no listing (nor load map) is provided. Any output will be written to the TAPE6=OUTPUT file. The following example is a set of control cards to load an E-net on KRONOS permanent file TAPE7 and to interpret the net with EXTF source code on the KRONOS permanent file named EXTCODE.

```

RFL (40000)
GET(EXTCODE)
RUN(I=EXTCODE)
GET(INTERP/UN=X652)
COPY(LG,INTERP)
GET(TAPE1=TAPE7)
RFL(46000)
INTERP.

```
The following description of interpreter specifications is passed to the program via the TAPE5=INPUT file. The numerical order of specifiers is important, and must not be violated; the INTERPRET and STOP specifiers are mandatory, and all others are optional, (with default values as shown).

1. INTERPRET (net name)
   Required. Specifies the name of the evaluation net load module found on TAPE1.

2. OUTPUT (=)
   Optional, the default causes INTERP to print only the list of currently active transitions at simulation termination.

   *P The output includes the default option plus summary statistics for each location. Total location dwell time, token throughput for each location, and the average residency time of each token on the location are provided. If the location is non-empty at termination time, the attributes of the resident token are also given. If the resident token is on an absorber location, average attribute values are also printed. The termination value of each environment variable is also printed.

   *F All *P output is given along with a full trace of the transition firing sequence, (i.e. the simulation time for each enabled phase and termination phase is given).

3. MARK
   Optional. Specifies the marking of the net.
   Location Bi(j) is marked with the following text entered after the MARK card.

   \[ Bi:1=\text{value}, 2=\text{value}, \ldots, j=\text{value}; \]
   Location marking is terminated with a new card with END.
4. **ENVIRØ**

   Optional. Used to specify initial values for environment variables.
   To mark a variable named VAR, include the following after the ENVIRØ entry:

   \[
   \text{VAR} = \text{<value>};
   \]

   This list is terminated by END.

5. **START=</integer time>**

   Optional. Default value is zero. Specifies the initial time assigned to the simulation clock.

6. **STOP=</integer time>**

   Required. Specifies the time at which the simulation terminates.

   Table V.1 is a list of initial specification errors that INTERP can detect. Run time errors are self descriptive, and indicate errors such as token storage array overflow, empty event list (i.e. no transition can fire), etc.

   Table V.2 is a sample listing for simulation of the net given in section III. The start time is 100, and the stop time is 400, and the output option is F (full).
Table V.1
Initial Specification Errors

1. INVALID DATA CARD—CALLED FROM DATCARD
2. <Name> LOCATION IS NOT IN THE SUB-NET (DATCARD)
3. <msg> IS IN AN INVALID NUMERIC FIELD ON A DATA CARD (DATCARD)
4. INTERPRET INVALID INTERPRET (1fn) CARD (INTCARD)
5. INVALID MARK CARD (MARCARD)
6. <name> LOCATION IS NOT IN THE SUB-NET (MARCARD)
7. EXCESSIVE NUMBER OF INITIAL MARKINGS (MARCARD)
8. <name> mark specification is bad (MARCARD)
9. <msg> IS PART OF AN INVALID NUMERIC FIELD ON MARK CARD (MARCARD)
10. <name> LOCATION HAS AN ATTRIBUTE THAT EXCEEDS THE DIMENSION (MARCARD)
11. ATTEMPT TO READ PAST END-OF-FILE FATAL ERROR (MEXT)
12. INVALID OUTPUT CARD (OUTCARD)
13. INVALID START CARD (SSCARDS)
14. BAD NUMERIC FIELD IN START CARD (SSCARDS)
15. STOP TIME OMITTED (SSCARDS)
16. INVALID OUTPUT CARD (SSCARDS)
17. BAD NUMERIC FIELD IN START CARD (SSCARDS)
INTERPRETER SPECIFICATIONS:

? INTERPRET(2SERVER)
  INTERPRET(2SERVER)
? OUTPUT(F)
  OUTPUT(F)
? MARK
  MARK
  B2: 1=0, 2=200;
  B2: 1=0, 2=200;
? END
  END
? ENVIR=1RO
  ENVIR
? MNAR=75
  MNAR=75
? END
  END
? START=100
  START=100
? STOP=400
  STOP=400
Table V.2(c)

IE-NET SIMULATION CONCLUSION AT 400
START= 100  UP TIME= 300

LOCATION SUMMARY

<table>
<thead>
<tr>
<th>NAME</th>
<th>TSA PTR</th>
<th>NO</th>
<th>VALUE</th>
<th>TIME</th>
<th>THRU PUT</th>
<th>AVG VALUE</th>
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</table>

EVENT SCHEDULE:

TIME  TRANS  FIRE
413   A2    A TO C,D
467   A6    A TO C
518   A7    A TO C

STOP
Appendix A

Implementation versus Design

The most significant deviation of the implementation of evaluation nets from their design is in the syntax specification of the net. The implementation syntax is fully defined in Appendix B. This modification was necessary to make the use of ENSS easier, not the implementation. The original procedure declarations were similar to LISP conditional expressions, while the implementation employs conditional expressions similar to those found in ALGOL-60 programs. The spirit of the procedure declaration is preserved.

A serious deviation from the design is that resolution procedures are evaluated from right-to-left in the implementation, whereas the design of resolution procedure evaluation used a left-to-right process. For example, suppose

\[ \psi(r_i) = r_i : \text{IF true THEN } r_i := 0 \text{ IF true THEN } r_i := 1; \]

evaluates \( r_i \) to 1 in the implementation and \( r_i \) to 0 in the design.

The design of evaluation nets indicated that during transition firing termination, the net programmer could assume that a copy of the token on the input location also existed simultaneously on the output location; the input location actually destroyed its token copy at the end of the transition procedure call. In the ENSS implementation, two copies do not simultaneously exist; the token is moved to the output location before the transition procedure is executed. The significance of this deviation is better understood by considering a transition procedure for a T transition, \( T(B_i,B_j) \):

\[ B_j(3) := f(B_i(k)) \]

will always set \( B_j(3) \) to zero in the implementation, whereas the design implies that the above expression is equivalent to:

\[ B_j(3) := f(B_j(k)). \]

Environment variables must be simple variables in the ENSS implementation; arrays of environment variables cannot be employed by the net programmer.
Appendix B

Evaluation Net Syntax

Assume the net is given by \(((L,P,R,A),M_0,((\xi,\omega))\)

<evaluation net>::=<location declaration section><Environment variable
declaration section><Resolution procedure declaration section>
<transition declaration section>

<location declaration section>::=TYPE \[L\lozenge:\text{declaration list}];

<peripheral location declaration section>

<declaration list>::=<location variable>,<declaration list>|<location variable>

<peripheral location declaration section>::= TYPE \[\text{PERIPH}:<\text{input location list}>
<\text{output location list}><\text{resolution location list}>;

<input location list>::=\text{INPUT}=<\text{simple location variable list}>|\text{null}

<output location list>::=\text{OUTPUT}=<\text{simple location variable list}>|\text{null}

<resolution location list>::= \text{RES}L=<\text{simple rloc variable list}>|\text{null}

<simple location variable list>::=

<location name>,<\text{simple location variable list}>

|<location name>

<simple rloc variable list>::=<resolution location name>,

<\text{simple rloc variable list}>

|<resolution location name>

<environment variable declaration section>::=

\text{TYPE }\text{ENVIR}O:<\text{environment variable list}>;|\text{null}

<environment variable list>::=<environment variable name>,

<\text{environment variable list}>

|<environment variable name>
<resolution procedure declaration section>::=
    TYPE \_\_ RES\_L:<resolution procedure declaration list>

<resolution procedure declaration list>::=<resolution procedure declaration>
    |<resolution procedure declaration list>

<resolution procedure declaration>::=
    <resolution location name>:<conditional list>;
    |<resolution location name>:<expression list>;

<transition declaration section>::=TYPE \_\_ TRANS:
    <transition declaration list>END

<transition declaration list>::=<transition declaration>
    |<transition declaration list>

<transition declaration>::=<transition name>:<transition body>;

<transition body>::=<transition head><transition tail>|<transition head>

<transition tail>::=,<transition procedure>|null

<transition procedure>::=<expression list><conditional list><else clause>
    |<conditional list><else clause>|<expression list>|null

<transition head>::=<T transition>|<and transition>|<or transition>

<T transition>::= T(<location name>,<location name>)<simple time expression>

<and transition>::= F<and schema>|J<and schema>

<and schema>::=(<location list>)<simple time expression>

<location list>::=,<location name>,<location name>,<location name>

<or transition>::= X<or schema>|Y<or schema>

<or schema>::=(<resolution location name>,<location list>)<complex time expression>

<complex time expression>::=,<right part>,<right part>|null

<simple time expression>::=,<right part>|null
<conditional list>::=<conditional expression><conditional list>
   |<conditional expression>
<conditional expression>::= IF<predicate>THEN<expression list>
<conditional tail>::=<expression list>ELSE<expression list>
   |<expression list>
   |<expression list><conditional expression>
<predicate>::=<boolean factor>|<predicate>v<boolean factor>
<boolean factor>::=<relation>|<boolean factor>a<relation>
<relation>::=<right part><relational operator><right part>
   |<simple predicate>
<relational operator>::=|>||=|<|\#
<simple predicate>::= EMPTY(<location name>)|FULL(<location name>)
<expression list>::=<expression>,<expression list>|<expression>
<expression>::=<left part><right part>|<vector copy>
<vector copy>::=<location name>::=<location name>
<left part>::=<simple variable>::=
<right part>::=<term>|<add operator><term>
   |<right part><add operator><right part>
<term>::=<factor>|<term><mult operator><factor>
<factor>::=<primary>|<factor>†<primary>
<primary>::=<unsigned number>|<simple variable>|<function call>
<mult operator>::=\*|/
<add operator>::= +|-<location variable>::=<location name>(<unsigned integer>)
<location name> ::=<identifier>εL
<resolution location name>::=<identifier>εR
<environment variable name>::=<identifier>εΣ
<transition name>::=<identifier>εA
/simple variable>::=<location variable> | <environment variable name>
<identifier>::=<alphafirst><alpha><alpha><alpha>
<alphafirst>:: A|B|C|...|X|Y|Z
,alpha>::=<alpha first>|null
/function call>::=RANDOM(<right part>) | LOG(<right part>)
          | EXP(<right part>) | EXTF(<right part>,<right part>)
<number>::=<unsigned number>|<unsigned number>
<unsigned number>::=<unsigned integer>|<decimal fraction>
                        |<unsigned integer><decimal fraction>
<decimal fraction>::= .<unsigned integer>
<unsigned integer>::=<digit>|<unsigned integer><digit>
<digit>::= 0|1|2|...|9
Appendix C
ENSS Abstract Machine

ENSS employs an abstract machine to simulate the operation of an evaluation net. The abstract machine can be thought of as containing two levels, one to handle token movement through an evaluation net, and the other to evaluate time expressions and resolution and transition procedures.

The token machine directly simulates the net as defined by the transition schema and the initial marking. The compiler produces a location table and a transition table which totally describe the physical structure of the net and the identification of tokens with their values. The tables are not fully initialized until a marking for the net is declared; the interpreter handles this task and then immediately begins simulating token activity.

The expression evaluation abstract machine again uses the transition and location tables, as well as a token storage array. Figure C.1 shows the logical relationship among the various abstract machine components. Instruction words are composed of an operation code and a maximum of two operands. The "control unit" and the "arithmetic-logic unit" are combined into a single set of subroutines which fetch an instruction word from the JTRIP array, masks out the three fields, and executes the instruction on data obtained from the token storage array via the LOC array. The indirect addressing through the LOC array is useful for handling token movement and dynamic dimensioning. The token storage array is a linked list of five-word elements; whenever a token moves to a new location with a larger or smaller dimension, token array storage blocks are allocated or deallocated, respectively. JTRAN contains all information about the particular transition being simulated; thus it is used in resolution procedure evaluation for X or Y transitions and in time expression evaluation and transition procedure evaluation for all transitions. The machine evaluates expressions using a stack (called the REG array). The
result of an evaluation leaves the final result in REG(1), also called the accumulator, ACC. Operand types are assumed to be floating point in all operations.

Table C.1 is a dump of the load module produced by the compiler, given the net declaration of Table III.2.

The following list of instructions can be executed by the abstract machine:

(1) Return

No operands. Indicates the completion of a procedure.

(2) Set Logic

No operands. Sets the Logic Register to zero (false).

(3) Branch on Zero, <address>

Branch to the <address> in the JTRIP array if the ACC is zero (false); else set the Logic Register non-zero (true) and continue executing instructions sequentially.

(4) Unary Minus, <i>

\[ \text{ACC} \leftarrow C(\text{LOC}<i>) \]

(5) Store, <i>,<j>

\[ \text{LOC}<i> \leftarrow C(\text{LOC}<j>) \]

(If i<0 then LOC<i> \equiv \text{REG}(-i))

(6) Add, <i>,<j>

\[ \text{ACC} \leftarrow C(\text{LOC}<i>) + C(\text{LOC}<j>) \]

(7) Subtract, <i>,<j>

\[ \text{ACC} \leftarrow C(\text{LOC}<i>) - C(\text{LOC}<j>) \]

(8) Multiply, <i>,<j>

\[ \text{ACC} \leftarrow C(\text{LOC}<i>) \times C(\text{LOC}<j>) \]

(floating point quotient)

(9) Divide, <i>,<j>

\[ \text{ACC} \leftarrow C(\text{LOC}<i>) / C(\text{LOC}<j>) \]

(10) Exponentiate, <i>,<j>

\[ \text{ACC} \leftarrow C(\text{LOC}<i>) \times C(\text{LOC}<j>) \]

(11) Logical AND, <i>,<j>

\[ \text{ACC} \leftarrow C(\text{LOC}<i>) \times C(\text{LOC}<j>) \]

*The expression "C(LOC i )" is a shorthand notation meaning the contents of the token attribute pointed to by LOC(i,1), where i is a location index.
(12) Logical OR, <i>, <j>
    \[ \text{ACC} \leftarrow \text{C(LOC}<i>) \lor \text{C(LOC}<j>) \] 

(13) Less, <i>, <j>
    \[ \text{ACC} \leftarrow \text{if} \ C(\text{LOC}<i>) < C(\text{LOC}<j>) \]
    \[\text{then} \ \text{TRUE} \ \text{else} \ \text{FALSE}\] 

(14) Less Equal, <i>, <j>
    \[ \text{ACC} \leftarrow \text{if} \ C(\text{LOC}<i>) \leq C(\text{LOC}<j>) \]
    \[\text{then} \ \text{TRUE} \ \text{else} \ \text{FALSE}\] 

(15) Equal, <i>, <j>
    \[ \text{ACC} \leftarrow \text{if} \ C(\text{LOC}<i>) = C(\text{LOC}<j>) \]
    \[\text{then} \ \text{TRUE} \ \text{else} \ \text{FALSE}\] 

(16) Greater Equal, <i>, <j>
    \[ \text{ACC} \leftarrow \text{if} \ C(\text{LOC}<i>) \geq C(\text{LOC}<j>) \]
    \[\text{then} \ \text{TRUE} \ \text{else} \ \text{FALSE}\] 

(17) Greater, <i>, <j>
    \[ \text{ACC} \leftarrow \text{if} \ C(\text{LOC}<i>) > C(\text{LOC}<j>) \]
    \[\text{then} \ \text{TRUE} \ \text{else} \ \text{FALSE}\] 

(18) Not Equal, <i>, <j>
    \[ \text{ACC} \leftarrow \text{if} \ C(\text{LOC}<i>) \neq C(\text{LOC}<j>) \]
    \[\text{then} \ \text{TRUE} \ \text{else} \ \text{FALSE}\] 

(19) Logic Check, <address>
    \[ \text{if} \ \text{Logic Register} \neq 0 \]
    \[\text{then} \ \text{goto} <\text{address}>\] 

(20) Load, <i>
    \[ \text{ACC} \leftarrow C(\text{LOC}<i>) \] 

(21) Create, <i>, <j>, n
    Create a new token for LOC<i>;
    Copy n attributes from LOC<j> into LOC<i>. 

(22) Allocate, <m>, <i>
    Assign m blocks of token storage to LOC<i>. 

(23) Deallocate, <m>, <i>
    Deallocate m blocks of token storage from LOC<i>. 

(24) Destroy, <i>, <m>
    Destroy (i.e. release token storage)
    the token on LOC<i> which contains m blocks. 

(25) Y-transition, <rloc>
    LOC<rloc>, a resolution location, and
    ACC are set to zero if the the token is to
    be moved from the 0-input to the output loca-
    tion, and one if the token is to be moved
    from the 1-input to the output location.
(26) No Operation
Null.

(27) Move, \textless i\textgreater , \textless j\textgreater
Move the token from LOC\textless i\textgreater to LOC\textless j\textgreater.

(28) Jump, \textless address\textgreater
Unconditionally branch to JTRIP(address) for the next instruction.

(29) Load Clock
Loads ACC with the current value of the ENSS clock.

(30) Vector Copy, \textless i\textgreater , \textless j\textgreater
Copies all attributes of the token on LOC\textless i\textgreater to LOC\textless j\textgreater.

(31) Full Predicate, \textless i\textgreater
If LOC\textless i\textgreater contains a token, loads ACC with a non-zero value, otherwise ACC\textless 0\textgreater.

(32) Empty Predicate, \textless i\textgreater
If LOC\textless i\textgreater does not contain a token, loads ACC with a non-zero value, otherwise ACC\textless 0\textgreater.

(33) Random, \emptyset , \textless j\textgreater
The second operand is a constant located at LOC\textless j\textgreater which is used as the seed for a call to a pseudo random number generator. The result is left in ACC.

(34) Natural Log, \emptyset , \textless j\textgreater
\[ \text{ACC} = \log_{e}(C(\text{LOC}\textless j\textgreater)) \]

(35) Exponential Function, \emptyset , \textless j\textgreater
\[ \text{ACC} = e^{C(\text{LOC}\textless j\textgreater)} \]

(36) External Function, \textless i\textgreater , \textless j\textgreater
Calls a user-provided FORTRAN function subprogram named EXT_F with the following sequence:
\[ \text{ACC} + \text{EXT_F} (\text{FIX}(C(\text{LOC}\textless i\textgreater)), C(\text{LOC}\textless j\textgreater)) \]
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usage lists

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