

## Lecture 19: Descriptive Specifications

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## Today's Lecture

- Introduce Descriptive Specifications
  - Axiomatic
  - Algebraic
  - Tour of the RAISE system
    - Developed in Denmark
    - Sold to European Manufacturing companies
    - Using RAISE to create these types of specifications
      - Has a full tool suite

## Descriptive Specifications

- Focuses on Properties, Not Behaviors
  - A declaration of a result, rather than a sequence of actions that produce the result
- Formalisms
  - Logical
  - Algebraic
- Mathematical Foundations
  - Predicate logic, set theory, abstract algebra

## Logic Specifications

- Vocabulary of Logical Expressions
  - Variables, constants, predicates, functions
  - Connectives: and ( $\wedge$ ), or ( $\vee$ ), not ( $\neg$ ), implies ( $\Rightarrow$ ), equivalent ( $\equiv$ )
  - Quantifiers: exists ( $\exists$ ), for all ( $\forall$ )
- Combined with Vocabulary of Application
  - Example: set operators ( $\in$ ,  $\cup$ ,  $\cap$ , ...)
  - Example: ADT operators (Push, IsFull, ...)

# Logic Specifications

- Additional Notes
  - Variables are either *free* or *bound*
  - Expressions are *theories* in the logic
  - V&V amounts to *theorem proving*

# Creating Logic Specifications

- Helper Predicates and Functions
  - Define the base properties of interest
  - Modularize the specification
- Property Assertions
  - Preconditions and postconditions
  - Invariants

# RAISE

*Rigorous Approach to Industrial Software Engineering*

- A Method and a Language
- Specification Language: RSL
- Specifications Refined in Levels
  - Associated consistency proof obligations
- Proofs of Properties Aided by Tools

# RAISE Specification of POTS\*

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scheme POTS =

# RAISE Specification of POTS\*

scheme POTS =

class  
type

value

variable

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scheme POTS =

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scheme POTS =

class  
type Line,

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```
scheme POTS =  
  class  
    type Line,  
      Status = Line  $\vec{m}$  {On_Hook, Off_Hook},
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scheme POTS =  
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    type Line,  
      Status = Line  $\vec{m}$  {On_Hook, Off_Hook},  
      Calls = Line  $\vec{m}$  Line
```

# RAISE Specification of POTS\*

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scheme POTS =  
  class  
    type Line,  
      Status = Line  $\vec{m}$  {On_Hook, Off_Hook},  
      Calls = Line  $\vec{m}$  Line  
  value
```

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scheme POTS =  
  class  
    type Line,  
      Status = Line  $\vec{m}$  {On_Hook, Off_Hook},  
      Calls = Line  $\vec{m}$  Line  
  value go_off_hook : Line  $\rightarrow$  Unit,
```

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scheme POTS =  
  class  
    type    Line,  
           Status = Line  $\vec{m}$  {On_Hook, Off_Hook},  
           Calls = Line  $\vec{m}$  Line  
    value  go_off_hook : Line  $\rightarrow$  Unit,  
           go_on_hook : Line  $\rightarrow$  Unit,
```

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  class  
    type    Line,  
           Status = Line  $\vec{m}$  {On_Hook, Off_Hook},  
           Calls = Line  $\vec{m}$  Line  
    value  go_off_hook : Line  $\rightarrow$  Unit,  
           go_on_hook : Line  $\rightarrow$  Unit,  
           place_call : Line  $\times$  Line  $\rightarrow$  Bool,
```

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           place_call : Line  $\times$  Line  $\rightarrow$  Bool,  
           end_call : Line  $\rightarrow$  Unit  
  
  variable
```

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      Status = Line  $\vec{m}$  {On_Hook, Off_Hook},  
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    value go_off_hook : Line  $\rightarrow$  Unit,  
          go_on_hook : Line  $\rightarrow$  Unit,  
          place_call : Line  $\times$  Line  $\rightarrow$  Bool,  
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    variable line_status : Status = [ L  $\mapsto$  On_Hook | L : Line ],
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    value go_off_hook : Line  $\rightarrow$  Unit,  
          go_on_hook : Line  $\rightarrow$  Unit,  
          place_call : Line  $\times$  Line  $\rightarrow$  Bool,  
          end_call : Line  $\rightarrow$  Unit  
    variable line_status : Status = [ L  $\mapsto$  On_Hook | L : Line ],  
          active_calls : Calls = [ ]
```

# RAISE Specification of POTS

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axiom

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axiom forall L, L<sub>1</sub>, L<sub>2</sub> : Line •

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go\_off\_hook(L)

go\_on\_hook(L)

place\_call(L<sub>1</sub>, L<sub>2</sub>)

end\_call(L)

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∧ L<sub>2</sub> ∉ dom active\_calls`

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∧ L<sub>2</sub> ∉ dom active\_calls` ∧ L<sub>2</sub> ∉ rng active\_calls`  
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place\_call(L<sub>1</sub>, L<sub>2</sub>) as S  
post S ⇒ L<sub>1</sub> ≠ L<sub>2</sub> ∧ active\_calls = active\_calls` † [ L<sub>1</sub> ↦ L<sub>2</sub> ]  
    ∧ L<sub>2</sub> ∉ dom active\_calls` ∧ L<sub>2</sub> ∉ rng active\_calls`  
pre line\_status(L<sub>1</sub>) = Off\_Hook

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    ∧ L<sub>2</sub> ∉ dom active\_calls` ∧ L<sub>2</sub> ∉ rng active\_calls`  
pre line\_status(L<sub>1</sub>) = Off\_Hook  
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    ∧ L<sub>2</sub> ∉ dom active\_calls` ∧ L<sub>2</sub> ∉ rng active\_calls`  
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end\_call(L)

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post

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```
end_call(L)
  post if L ∈ dom active_calls`
    then
    else

    end
```

# RAISE Specification of POTS

```
end_call(L)
  post if L ∈ dom active_calls`
    then active_calls = active_calls` \{ L }
    else

    end
```

## RAISE Specification of POTS

```
end_call(L)
  post if L ∈ dom active_calls`
    then active_calls = active_calls` \{ L }
    else ∃ L3 : Line •
      end
```

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end_call(L)
  post if L ∈ dom active_calls`
    then active_calls = active_calls` \{ L }
    else ∃ L3 : Line •
      active_calls` (L3) = L
      end
```

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  pre
```

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    then active_calls = active_calls` \{ L }
    else ∃ L3 : Line •
      active_calls` (L3) = L ∧ active_calls = active_calls` \{ L3 }
    end
  pre L ∈ dom active_calls
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    else ∃ L3 : Line •
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    end
  pre L ∈ dom active_calls ∨ L ∈ rng active_calls
```

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    then active_calls = active_calls` \{ L }
    else ∃ L3 : Line •
      active_calls` (L3) = L ∧ active_calls = active_calls` \{ L3 }
    end
  pre L ∈ dom active_calls ∨ L ∈ rng active_calls
end
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